

- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology



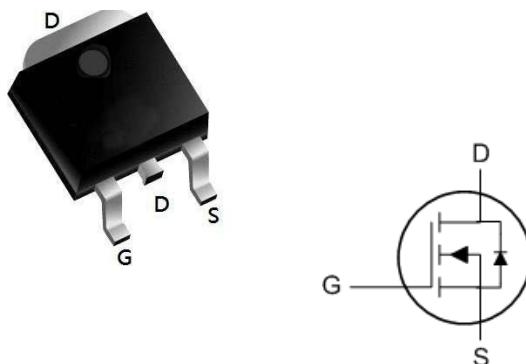
Product Summary

BVDSS	RDS(ON)	ID
60V	25mΩ	20A

Description

The XXW20N06 is the high performance complementary N-ch and P-ch MOSFETs with high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications. The XXW20N06 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

TO252 Pin Configuration



Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		60	V
V_{GSS}	Gate-Source Voltage		± 30	V
I_D	Continuous Drain Current	$T_c = 25^\circ\text{C}$	20	A
		$T_c = 100^\circ\text{C}$	10	A
I_{DM}	Pulsed Drain Current ^{note1}		80	A
EAS	Single Pulsed Avalanche Energy ^{note2}		39	mJ
P_D	Power Dissipation	$T_c = 25^\circ\text{C}$	41.7	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		50	$^\circ\text{C}/\text{W}$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +175	$^\circ\text{C}$

N-Ch 60V Fast Switching MOSFETs
Electrical Characteristics ($T_J = 25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	60	-	-	V
Gate-Body Leakage Current	I_{GSS}	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current $T_J=25^\circ\text{C}$	I_{DSS}	$V_{DS} = 60\text{V}, V_{GS} = 0\text{V}$	-	-	1	μA
$T_J=100^\circ\text{C}$			-	-	100	
Gate-Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.2	1.7	2.5	V
Drain-Source on-Resistance ⁴	$R_{DS(\text{on})}$	$V_{GS} = 10\text{V}, I_D = 10\text{A}$	-	25	32	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 5\text{A}$	-	31.5	40	
Forward Transconductance ⁴	g_{fs}	$V_{DS} = 5\text{V}, I_D = 10\text{A}$	-	15.5	-	S
Dynamic Characteristics⁵						
Input Capacitance	C_{iss}	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$	-	1355	-	pF
Output Capacitance	C_{oss}		-	60	-	
Reverse Transfer Capacitance	C_{rss}		-	49	-	
Gate Resistance	R_G	$f = 1\text{MHz}$	-	1.2	-	Ω
Switching Characteristics⁵						
Total Gate Charge	Q_g	$V_{GS} = 10\text{V}, V_{DD} = 30\text{V}, I_D = 10\text{A}$	-	22	-	nC
Gate-Source Charge	Q_{gs}		-	4.2	-	
Gate-Drain Charge	Q_{gd}		-	6.9	-	
Turn-on Delay Time	$t_{d(on)}$	$V_{GS} = 10\text{V}, V_{DD} = 30\text{V}, R_G = 3\Omega, I_D = 10\text{A}$	-	6.4	-	ns
Rise Time	t_r		-	15.3	-	
Turn-off Delay Time	$t_{d(off)}$		-	25	-	
Fall Time	t_f		-	7.6	-	
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 10\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	-	26	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	45	-	nC
Drain-Source Body Diode Characteristics						
Diode Forward Voltage ⁴	V_{SD}	$I_S = 10\text{A}, V_{GS} = 0\text{V}$	-	-	1.2	V
Continuous Source Current	I_S	-	-	-	20	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature $T_{J(\text{MAX})}=150^\circ\text{C}$
2. The EAS data shows Max. rating . The test condition is $V_{DD}=25\text{V}, V_{GS}=10\text{V}, L=0.4\text{mH}, I_{AS}=14\text{A}$
3. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
5. This value is guaranteed by design hence it is not included in the production test.

Typical Characteristics

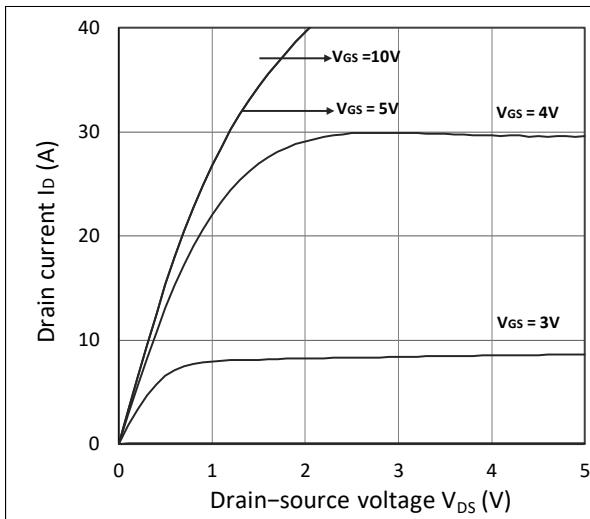


Figure 1. Output Characteristics

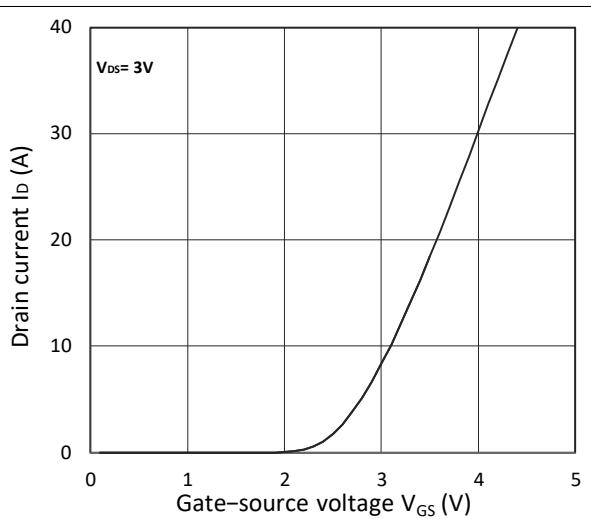


Figure 2. Transfer Characteristics

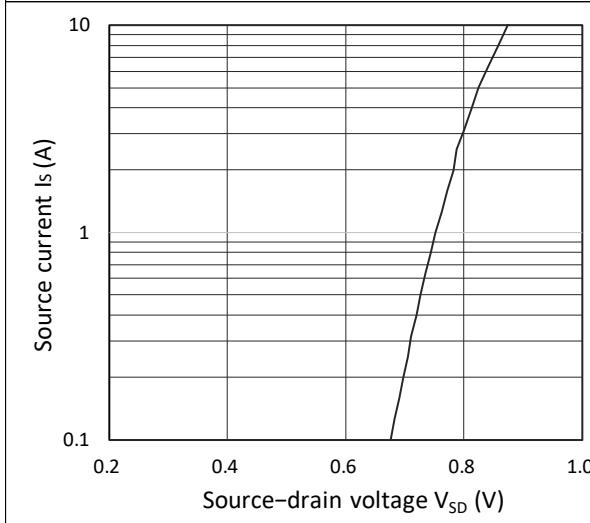


Figure 3. Forward Characteristics of Reverse

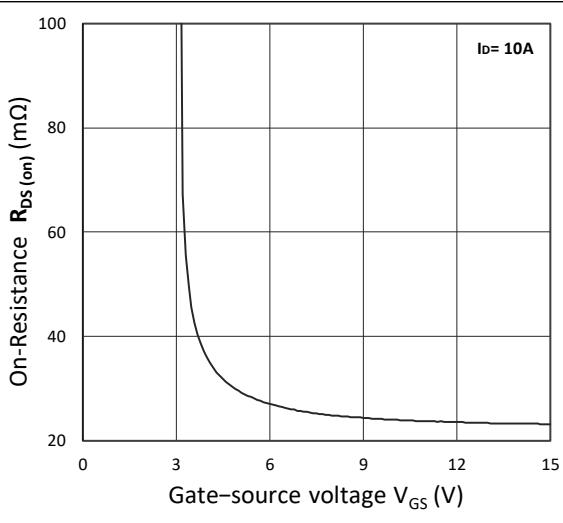


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

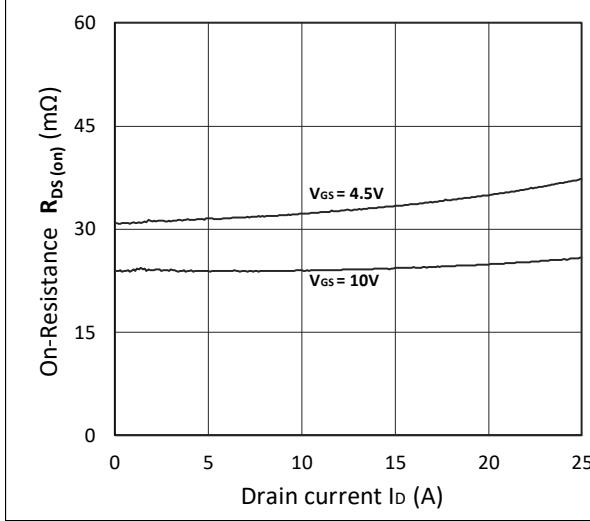


Figure 5. $R_{DS(ON)}$ vs. I_D

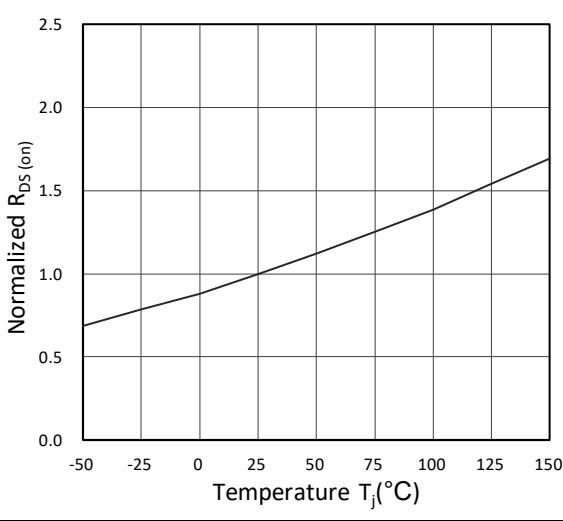


Figure 6. Normalized $R_{DS(ON)}$ vs. Temperature

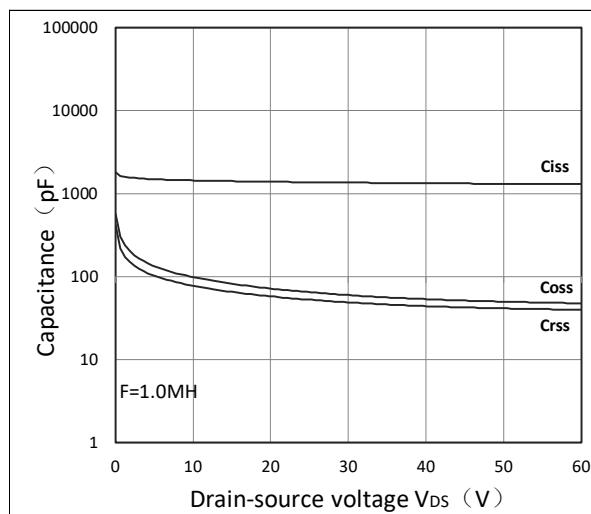


Figure 7. Capacitance Characteristics

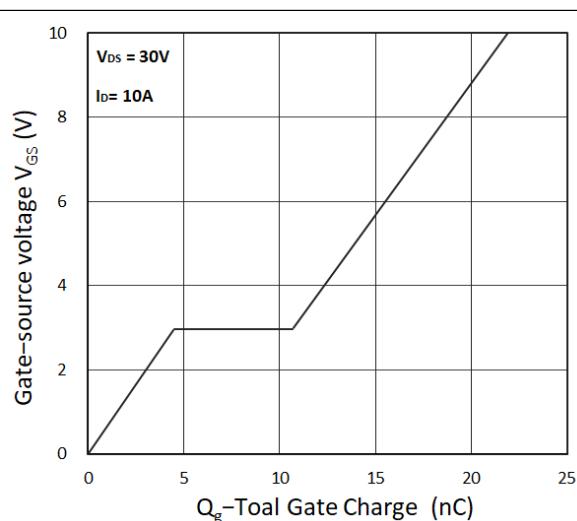


Figure 8. Gate Charge Characteristics

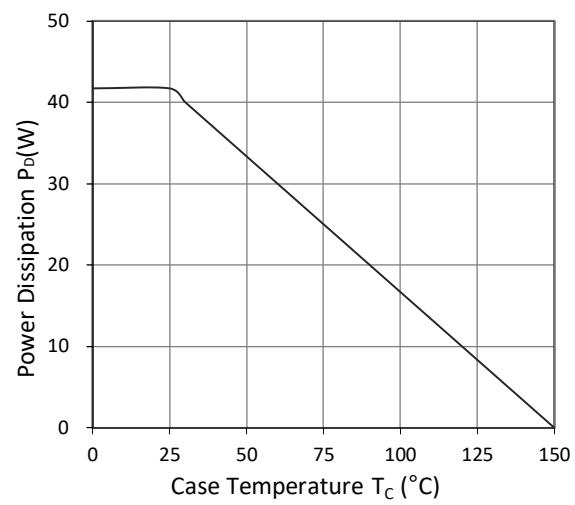


Figure 9. Power Dissipation

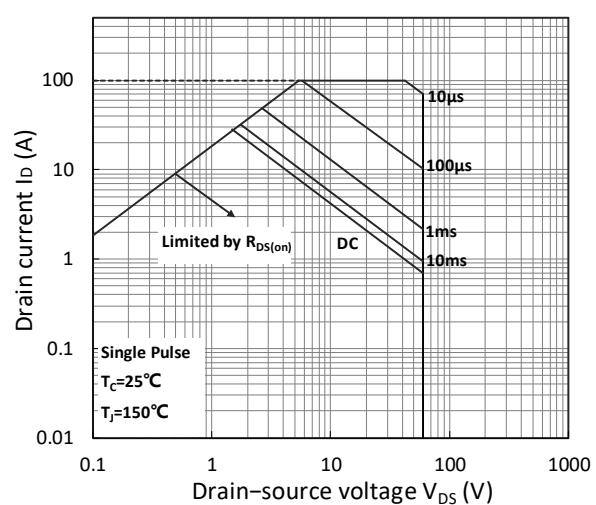


Figure10. Safe Operating Area

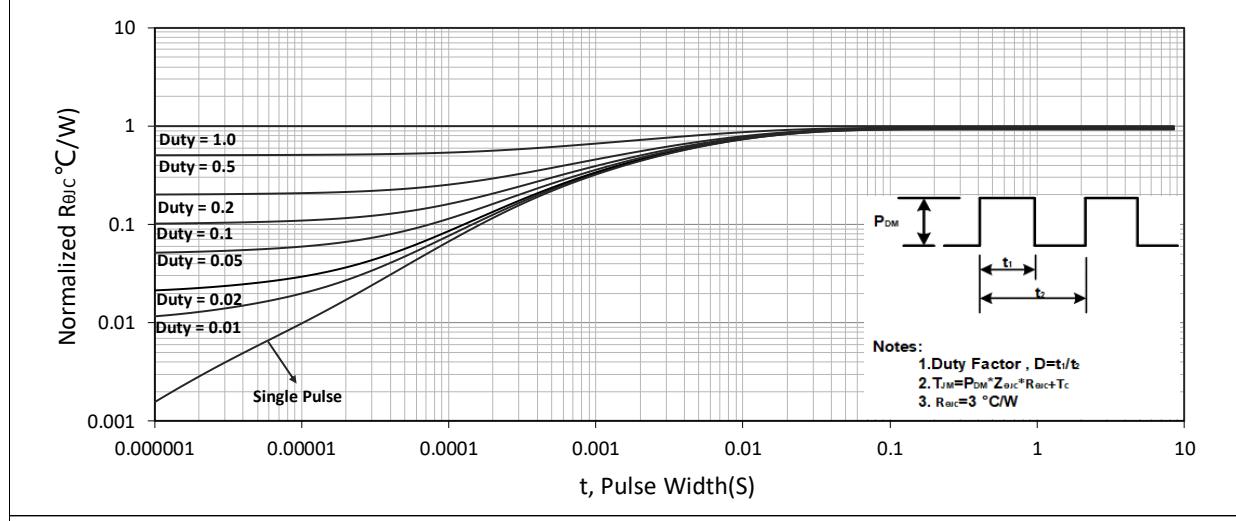
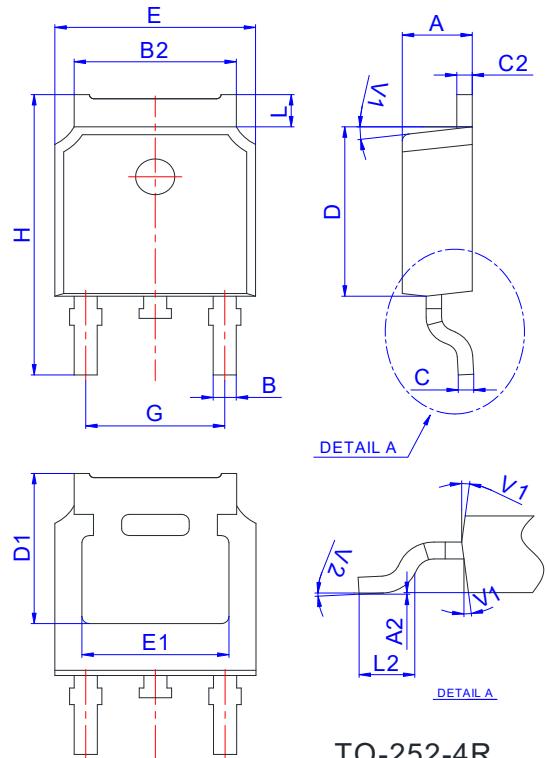


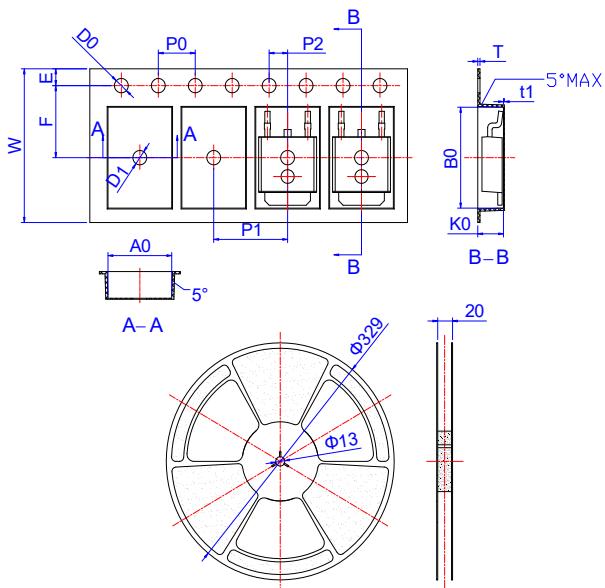
Figure 11. Normalized Maximum Transient Thermal Impedance

Package Mechanical Data-TO-252



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

Reel Specification-TO-252



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
W	15.90	16.00	16.10	0.626	0.630	0.634
E	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.40	1.50	1.60	0.055	0.059	0.063
D1	1.40	1.50	1.60	0.055	0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
A0	6.85	6.90	7.00	0.270	0.271	0.276
B0	10.45	10.50	10.60	0.411	0.413	0.417
K0	2.68	2.78	2.88	0.105	0.109	0.113
T	0.24		0.27	0.009		0.011
t1	0.10			0.004		
10P0	39.80	40.00	40.20	1.567	1.575	1.583